Several modifications have been made to transmitter since the original release of this reliability prediction. This revision to ATM-856 updates that prediction to correspond the current transmitter circuit diagram depicted in Bendix drawing 2344610, Revision G (with ECN 2344610 G2).

The revisions to the transmitter circuit since the original release of the reliability prediction are documented in Bendix CRD's #57862 (to Rev. D), #57988 (to Rev. E), #58666 (to Rev. F), and #58679 (to Rev. G). These design changes were incorporated primarily to suppress conducted EMI and to improve the stability and tuning characteristics of the power amplifier in the transmitter.

The result of these circuit changes is a slight improvement in the overall transmitter reliability. For the transmitter, the new prediction is 0.98901 vs. 0.98889, based on the transmitter FMECA (ATM 854-Rev. A). In the standby redundant configuration this gives a new prediction of 0.99985.

Based on the Transmitter Parts Applications Analysis (ATM 841-Rev. B), the reliability prediction becomes 0.97492, up from 0.97410. This gives a stand-by redundant configuration prediction of 0.99932.

In summary, the modifications to Data Transmitter have not degraded the predicted reliability, and have in fact slightly improved the estimate.

Revised by: 
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Approved by: 
P. McGinnis 
ALSEP Reliability
1.0 Scope

This report presents the Data Transmitter reliability prediction estimates derived through the studies of Data Transmitter Failure Mode, Effects and Criticality Analysis (FMECA) and Data Transmitter Parts Application Analysis (PAA).

2.0 Conclusion

The Data Transmitter has reliability of .98901 for a one year mission which more than meets the design goal of .985.

The stand-by redundancy of the Data Transmitters (2) effected through the diplexer switch has a reliability of .99985 which is indeed very close to unity.

3.0 Reliability Prediction Estimates

Figure 1 presents the Data Transmitter Reliability Block Diagram, and gives the prediction estimates for the Transmitter (.98901) and of its constituent subassemblies. Figure 2 presents the Data Transmitter Reliability Block Diagram in the standby redundant configuration, and gives the prediction estimate for this configuration as .99985.

The prediction estimates given in Figures 1 & 2 are derived through the Data Transmitter FMECA described in ATM 854 Rev. A and dated 15 June 1970. This study has utilized the reliability data of the Transmitter Parts Application Analysis (PAA) on a failure mode basis and not on a parts count basis.

Figure 3 presents the same Reliability Block Diagram of Figure 1, and gives the prediction estimates derived through Data Transmitter PAA, described in ATM 841B and dated 15 June 1970. Similarly, Figure 4 gives the Transmitter prediction estimate for the redundant configuration (.99932). The predictions derived through the PAA analysis are based on the Transmitter total parts count and the failure rates for each and every part.
Data Transmitter Reliability = .98901

Figure 1. Data Transmitter Reliability Block Diagram.

Reliability Prediction per Failure Mode & Effects Analysis
Reliability of Redundant Transmitters: $R$

$$R = R_t + R_{ss} R_t \times \lambda t \sim R_t \times R_{ss} R_t (1 - R_t)$$

$$= .98901 + .99777 \times .98901 \times .01099$$

$$= .99985$$

*Figure 2. Reliability Block Diagram of Redundant Transmitters.*

*Reliability Prediction per Failure Mode & Effects Analysis.*
Data Transmitter Reliability = 0.97492
\[ \lambda = 2.8979 \times 10^{-5} \]

<table>
<thead>
<tr>
<th>Component</th>
<th>Reliability</th>
<th>Failure Rate</th>
</tr>
</thead>
<tbody>
<tr>
<td>OSC.-Buffer-Multiplier</td>
<td>R = 0.99273</td>
<td>( \lambda = 0.08304 \times 10^{-5} )</td>
</tr>
<tr>
<td>Modulator</td>
<td>R = 0.99533</td>
<td>( \lambda = 0.05332 \times 10^{-5} )</td>
</tr>
<tr>
<td>Preamplifier</td>
<td>R = 0.99783</td>
<td>( \lambda = 0.02477 \times 10^{-5} )</td>
</tr>
<tr>
<td>Telemetry</td>
<td>R = 0.99247</td>
<td>( \lambda = 0.08619 )</td>
</tr>
<tr>
<td>X 6 Multiplier</td>
<td>R = 0.99597</td>
<td>( \lambda = 0.04605 \times 10^{-5} )</td>
</tr>
<tr>
<td>Power Amplifier</td>
<td>R = 0.99278</td>
<td>( \lambda = 0.08261 \times 10^{-5} )</td>
</tr>
</tbody>
</table>

Figure 3. Data Transmitter Reliability Block Diagram.

Reliability Prediction per MIL-STD-756A & PAA
Reliability of Redundant Transmitters: \( R \)

\[
R = R_t + R_{ss} R_t \times \lambda_t \sim R_t + R_{ss} R_t \left(1 - R_t\right)
\]

\[
= .97492 + .99777 \times .97492 \times .02508
\]

\[
= .99932
\]

Figure 4. Reliability Block Diagram of Redundant Transmitters.

Reliability Prediction per MIL-STD-756A & PAA